



218242

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

KALKUNTE et al.

Atty. Dck. No. 108339-00074

SEP 1 0

SEP 1 0 2001

rechnology Center 2100

Serial No.: 09/877,069

Filed: June 11, 2001

For: TRUNKING AND MIRRORING ACROSS STACKED GIGABIT SWITCHES

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents Washington, D.C. 20231

Date: September 7, 2001

Sir:

Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the information item(s) listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each item(s) is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the item(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

merits	This Information Disclosure Statement is being filed (a) within three months U.S. filing date, OR (b) before the mailing date of a first Office Action on the in the present application, OR (c) accompanies a Request for Continued nation. No certification or fee is required.
	This Information Disclosure Statement is being filed more than three months ne U.S. filing date AND after the mailing date of the first Office Action on the , but before the mailing date of a Final Rejection or Notice of Allowance.
	a. I hereby certify that each item of information contained in this Information Disclosure Statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. 37 CFR §1.97(e)(1).
	b. I hereby certify that no item of information in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to my knowledge after making reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this Information Disclosure Statement. 37 CFR §1.97(e)(2).
	c. A check in the amount of \$180.00 in payment of the fee under 37 CFR §1.17(p). Please charge any fee deficiency or credit any overpayment to Deposit Account No. 01-2300 as needed to ensure consideration of the

disclosed information.

	Allowa the Inf amour charge	This Information Disclosure Statement is being filed more than three months ne U.S. filing date and after the mailing date of a Final Rejection or Notice of ince, but before payment of the Issue Fee. Applicant(s) hereby petition(s) that formation Disclosure Statement be considered. Attached is our check in the nt of \$180.00 in payment of the petition fee under 37 CFR §1.17(i)(1). Please any fee deficiency or credit any overpayment to Deposit Account No. 01-2300 eded to ensure consideration of the disclosed information.
		a. I hereby certify that each item of information contained in this Information Disclosure Statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. 37 CFR §1.97(e)(1).
		b. I hereby certify that no item of information in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to my knowledge after making reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this Information Disclosure Statement. 37 CFR §1.97(e)(2).
\boxtimes	4. are att	An English-language Abstract(s) of the non-English language reference(s) ached hereto.

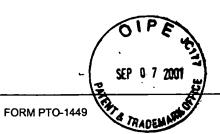
Respectfully submitted,

Douglas H. Goldhush Registration No. 33,125

Customer No. 004372 ARENT FOX KINTNER PLOTKIN & KAHN, PLLC 1050 Connecticut Avenue, N.W., Suite 400 Washington, D.C. 20036-5339

Tel: (202) 857-6000 Fax: (202) 638-4810

DHG/mzk



Sheet 1 of 3

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

ATTY. DOCKET NO. SERIAL NO.

LIST OF REFERENCES CITED BY APPLICANT

(Use several sheets if necessary)

108339-00074	09/877,069
APPLICANT	
KALKUNTE et al.	
FILING DATE	GROUP
June 11 2001	

U.S. PATENT DOCUMENTS

EVALUED	1		1 0.0.17	TENT DOCUMENTS		CUD	FILING
EXAMINER INITIAL		DOCUMENT NO.	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
	AA	5,555,398	Sep. 10, 1996	Raman			Apr. 15, 1994
	AB	5,644,784	Jul. 1, 1997	Peek			Mar. 3, 1995
	AD	5,828,653	Oct. 27, 1998	Goss			Apr. 26, 1996
	AE	5,423,015	Jun. 6, 1995	Chung			May 21, 1991
	AF	5,696,899	Dec. 9, 1997	Kalwitz	·		Nov. 18, 1992
	AG	6,061,351	May 9, 2000	Erimli et al.			Dec. 18, 1997
	АН	5,748,631	May 5, 1998	Bergantino et al.			May 9, 1996
	Al	6,119,196	Sep. 12, 2000	Muller et al.			Jun. 30, 1997
	AJ	6,175,902 B1	Jan. 16, 2001	Runaldue et al.			Dec. 18, 1997
	AK	5,473,607	Dec. 5, 1995	Hausman et al.			Aug. 9, 1993
-	AL	5,787,084	Jul. 28, 1998	Hoang et al.			Jun. 5, 1996
	AM	5,845,081	Dec. 1, 1998	Rangarajan et al.			Sep. 3, 1996
	AN	5,892,922	Apr. 6, 1999	Lorenz			Feb. 28, 1997
	AO	5,802,287	Sep. 1, 1998	Rostoker et al.			Aug. 3, 1995
	AP	6,011,795	Jan. 4, 2000	Varghese et al.			Mar. 20, 1997
	AQ	6,185,185 B1	Feb. 6, 2001	Bass et al.			Nov. 21, 1997
	AR	5,524,254	Jun. 4, 1996	Morgan et al.			Jul. 1, 1994
	AS	5,987,507	Nov. 16, 1999	Creedon et al.			Dec. 29, 1998
- "	AT	5,831,980	Nov. 3, 1998	Varma et al.			Sep. 13, 1996
	AU	5,781,549	Jul. 14, 1998	Dai			Feb. 23, 1996
	AV	6,041,053	Mar. 21, 2000	Douceur et al.			Sep. 18, 1997
	AW	5,459,717	Oct. 17, 1995	Mullan et al.			Mar. 25, 1994
	AX	5,909,686	Jun. 1, 1999	Muller et al.			Jun. 30, 1997
	AY_	5,887,187	Mar. 23, 1999	Rostoker et al.			Apr. 14, 1997
	AZ_	5,568,477	Oct. 22, 1996	Galand et al.			Jun. 27, 1995
	BA	5,414,704	May 9, 1995	Spinney			Apr. 5, 1994
	BB	5,390,173	Feb. 14, 1995	Spinney et al.			Oct. 22, 1992
	ВС	5,825,772	Oct. 20, 1998	Dobbins et al.			Apr. 2, 1996
	BD_	5,790,539	Aug. 4, 1998	Chao et al.			Jan. 29, 1996

U.S. PATENT DOCUMENTS Sheet <u>2</u> of <u>3</u> EXAMINER INITIAL SUB-**FILING** CUMENT CLASS DATE NAME **CLASS** DATE 5,918,074 Jun 29, 1999 Jul. 25, 1997 8E Wright et al. BF 5,898,687 Harriman et al. Jul. 24, 1996 Apr. 27, 1999 BG 5,940,596 Aug. 17, 1999 Aug. 4, 1997 Rajan et al. ВН Jun. 26, 1996 5,802,052 Sep. 1, 1998 Venkataraman ы 5,842,038 Williams et al. Oct. 10, 1996 Nov. 24, 1998 BJ 5,742,613 Apr. 21, 1998 MacDonald Apr. 27, 1993 BK Nov. 26, 1996 Ganson et al. Feb. 28, 1994 5,579,301 Jan. 11, 1994 Dec. 6, 1991 BL 5,278,789 Inoue et al. ВМ 5,652,579 Jul. 29, 1997 Yamada et al. Aug. 15, 1996

Cooper

Aug. 31, 1993

			FOREIGN P	ATENT DOCUMENTS					
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB- CLASS	TR/ YES	ANSLA NO	TION PART.
•	во	WO9900936A1	Jun. 12, 2001	WIPO					
,	BP	EP0465090A1	Jan. 8, 1992	EPO					
	BQ	EP0859492A2	Aug. 19, 1998	EPO					
J	BR	EP0312917A2	Apr. 26, 1989	EPO '					
•	BS	EP0853441A2	Jul. 15, 1998	EPO					
٠.	ВТ	4-189023	Jul. 7, 1992	Japan					Abs
	BU	WO 98/09473	Mar. 5, 1998	WIPO					
	BV	EP0752796A2	Jan. 8, 1997	EPO					
4	вw	EP0862349A2	Sep. 2, 1998	EPO					Abs
	вх	FR 2 725 573 - A1	Apr. 12, 1996	France					Abs
	BY	EP0854606A2	Jul. 22, 1998	EPO					
•	BZ	WO 99/00948	Jan. 7, 1999	WIPO					
4	CA	WO 99/00949	Jan. 7, 1999	WIPO					
,	СВ	WO 99/00950	Jan. 7, 1999	WIPO					
-	СС	WO 99/00939	Jan. 7, 1999	WIPO					
c	CD	WO 99/00938	Jan. 7, 1999	WIPO					
	CE	WO 99/00944	Jan. 7, 1999	WIPO					
	CF	WO 99/00945	Jan. 7, 1999	WIPO					
•	CĠ	EP0849917A2	Jun. 24, 1998	EPO					
	СН	EP0907300A2	Apr. 7, 1999	EPO					

BN

5,499,295

Mar. 12, 1996

•		OF LER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.) Sheet 3 of 3
	CI	"A High-Speed MOS Circuit for 1.2-Gb/s 16 x 16 ATM Switching," Alain Chemarin et al. 8107 IEEE Journal of Solid-State Circuits 27(1992) July, No. 7, New York, US, pages 1116-1120
	cu	"Local Area Network Switch Frame Lookup Technique for Increased Speed and Flexibility," 700 IBM Technical Disclosure Bulletin 38(1995) July, No. 7, Armonk, NY, US, pages 221-222
	СК	"Queue Management for Shared Buffer and Shared Multi-buffer ATM Switches," Yu-Sheng Lin et al., Department of Electronics Engineering & Institute of Electronics, National Chiao Tung University, Hsinchu, Taiwan, R.O.C., March 24, 1996, pages 688-695
	CL	"A 622-Mb/s 8 x 8 ATM Switch Chip Set with Shared Multibuffer Architecture," Harufusa Kondoh et al., 8107 IEEE Journal of Solid-State Circuits 28(1993) July, No. 7, New York, US, pages 808-814
	СМ	"Catalyst 8500 CSR Architecture," White Paper XP-002151999, Cisco Systems Inc. 1998, pages 1-19
	CN	"Computer Networks," A.S. Tanenbaum, PRENTICE-HALL INT., USA, XP-002147300(1998), Sec. 5.2-Sec. 5.3, pages 309-320

EXAMINER		DATE CONSIDERED
*EXAMINER:	Initial if reference considered, whether or not citation is in conformance and not considered. Include copy of this for	n conformance with MPEP 609; Draw line through citation if not in orm with next communication to applicant.